



Vel Tech
Rangarajan Dr. Sagunthala
R&D Institute of Science and Technology
(Deemed to be University Estd. as per UGC Act, 1956)

End Semester Examinations through Online Mode
M.Tech.(Embedded Systems) Examinations
REGULAR COURSES
TIME TABLE JANUARY - 2022
Applicable for 2020 Admitted Batch Only

Duration: 01.30 Hrs

Time - FN : 10.00am to 11.30am

S.No	Date	Course Code & Name
1	19-01-2022 FN	2161EC109 - Software Technology for Embedded Systems
2	20-01-2022 FN	2162EC105 - Hardware Software Co-Design
3	24-01-2022 FN	2162EC111 - Internet of Things
4	21-01-2022 FN	2162EC137 - Real Time Systems with FPGA
5	22-01-2022 FN	2162EC141 - Multimedia Architectures

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Signature



CONTROLLER OF EXAMINATIONS

Signature

C.R. Bhat