Last Working Day - 28.11.2026

Vel Tech
Rangarajan Dr. Sagunthala
R&D Institute of Science and Technology
DEEMED TO BE
University
(Estd. u/s 3 of UGC Act 1956)
Avadi, Chennai

B.Tech. Electronics & Communication Engineering

Semester End Examinations Time Table Nov/Dec - 2026

Regular Courses

Applicable for 2024-25 Admitted Batch Only

Duration : 3Hrs

Time FN: 10.00 am to 01.00 pm
AN: 02.00 pm to 05.00 pm

S.No.	Date & Session	Course Code	Course Name
1	30.11.2026 FN	10211EC102	Analog Electronics
		10211EC104	Linear Integrated Circuits
		10211EC105	Control Systems
		10211EC202	Internet of Things
2	02.12.2026 FN	10211EC113	Antenna Theory
3	04.12.2026 FN	10211EC109	Microprocessor and Microcontroller
		10211EC110	Data Communication Networks
		10211EC111	Discrete Time Signal Processing
4	07.12.2026 FN	10211EC101	Circuit Theory
		10211EC108	Communication Systems
		10211EC112	Wireless Communication
5	09.12.2026 FN	10210ME103	Innovation and Entrepreneurship
		10213CS201	Artificial Intelligence
6	11.12.2026 FN	10210MA104	Fourier Series and Transform Techniques
		10210ME104	Project Management and Finance
7	12.12.2026 FN	10212EC102	Cellular Mobile Communication
		10212EC115	High Performance Communication Networks
		10212EC130	Solid State Devices
		10212EC138	Introduction to Artificial Intelligence and Machine Learning
		10212EC174	Principles of Data Science
		10212EC175	Fundamentals of Quantum Computing
		10212EC176	Industry 4.0 and IIoT
		10212EC219	Analog Circuit IC Design
		10212EC222	Principles of Networking and Cyber Security
		10212EC226	Machine Learning
		10212EC238	Soc Design and Verification

8	14.12.2026 FN	10211EC106	Signals and Systems
		10212EC139	Wireless Communications and Networking
		10212EC145	Digital IC Design
9	15.12.2026 FN	10211EC107	Electromagnetics and Transmission Lines
10	16.12.2026 FN	10211EC114	VLSI Design

Controller of Examinations (i/c)

CONTROLLER OF EXAMINATIONS