Last Working Day - 17.11.2026

Vel Tech
Rangarajan Dr. Sagunthala
R&D Institute of Science and Technology
DEEMED TO BE
University
Avadi, Chennai

B.Tech. Electronics & Communication Engineering

Semester End Examinations Time Table Nov/Dec - 2026

Regular Courses

Applicable for 2025-26 Admitted Batch Only

Duration : 2Hrs **Time FN: 10.00 am to 12.00 pm**

S.No.	Date & Session	Course Code	Course Name
1	24.11.2026 FN	2510MA1003	Discrete Mathematical Structures
		2510MA1004	Fourier Series and Transforms Techniques
2	26.11.2026 FN	2510GE1003	Universal Human Values
		2511EC2204	Analog Integrated Circuits
		2511EE1002	DC Machines and Transformers
3	28.11.2026 FN	2511CS1001	Advanced Data Structures
		2511EC1207	Circuit Theory and Network Analysis
		2511EE2001	Electromagnetic Fields
4	01.12.2026 FN	2511CS1007	Database Management System
		2511EC2205	Digital Logic Circuits Design
		2511EE1012	Electronic Circuits
5	03.12.2026 FN	2511CS1006	Programming using Java
		2511EC1206	Signals and Systems
		2511EE1001	Circuit Analysis
6	05.12.2026 FN	2511CS2001	Digital Electronics
		2512EE1073	Energy Storage Systems
7	08.12.2026 FN	2510EC2001	Basic Electrical and Electronics Engineering

C. N. Bhuthi

Controller of Examinations (i/c)

Vel Tech Dr. Sagunthala R&D Institute of Science and Technology
CONTROLLER OF EXAMINATIONS