Last Working Day - 17.11.2026

Vel Tech
Rangarajan Dr. Sagunthala R&D Institute of Science and Technology
DEEMED TO BE
University
Established U/S 3 of UGC Act, 1956
Avadi, Chennai

B.Tech. Electrical and Electronics Engineering

Semester End Examinations Time Table Nov/Dec - 2026

Regular Courses

Applicable for 2025-26 Admitted Batch Only

Duration : 2Hrs **Time FN: 10.00 am to 12.00 pm**

S.No.	Date & Session	Course Code	Course Name
1	24.11.2026 FN	2510MA1004	Fourier Series and Transforms Techniques
2	26.11.2026 FN	2511EE1002	DC Machines and Transformers
3	28.11.2026 FN	2511EE2001	Electromagnetic Fields
4	01.12.2026 FN	2511EE1012	Electronic Circuits
5	03.12.2026 FN	2511EE1001	Circuit Analysis
6	05.12.2026 FN	2512EE1017	Renewable Energy Sources
		2512EE1054	Computer Networks and Communication
		2512EE1073	Energy Storage Systems

C. N. Balaji

Controller of Examinations (i/c)

CONTROLLER OF EXAMINATIONS