

WHO CAN ATTEND

UG/PG Students , Faculty, Research Scholars Industrial Experts

RESOURCE PERSONS

Dr. S. Arumuga Perumal

Director – Riyasaa Labs, Nagearcoil.

Dr. Sk. Noor Mohammed

Department of CSE

Associate Professor, IIITDM- Kancheepuram.

Dr. V. Prakash Kodali

Department of ECE

Associate Professor, NIT- Warangal.

Dr. V. Masilamani

Department of ECE

Professor, IIITDM- Kancheepuram.

Mr. T. A. Bharathwaj

Senior Tech Lead, E-info Chips, Chennai.

Mr. R. Venkatesh

Director, Taras Systems and Solutions,
Bengaluru.

Registration FREE for all the participants

REGISTRATION LINK

<https://forms.gle/4v6cYt2HLCuPfyV2A>



DATES TO REMEMBER

Last date to register online - 17.07.2026

Intimation of Selection - 18.07.2026

Join our whatsapp group for updates

<https://chat.whatsapp.com/K6wVAAP0wig51b7kJsFNFF?s=cl&p=a&iir=0>

CONTACT DETAILS

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WITH THE BLESSINGS OF

Col. Prof. Vel Dr. R. Rangarajan

Founder President & Chancellor

Dr. Sagunthala Rangarajan

Foundress President

PATRON (S)

Mrs. Rangarajan Mahalakshmi K

Chair Person & Managing Trustee

Prof. Rajat Gupta

Vice Chancellor

CO - PATRONS

Prof. Dr. R. Sivaraman

Pro- Vice Chancellor

Prof. Dr. E. Kannan

Registrar

Prof. Dr. P. Sakthivel

Chairman IEEE Madras Section

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Dean Academics

Prof. Dr. P. Suresh

Dean - International Relations & HRDC

Prof. Dr. P. Chandrakumar

Dean R&D, Industry & Incubation

Prof. Dr. S. Radha

Secretary – IEEE Madras Section

Prof. Dr. S. Brindha

Treasurer - – IEEE Madras Section

CONVENOR(S)

Prof. Dr. R. S. Valarmathi

Dean - SoEC

Prof. Dr. S. Jana

Head - Department of ECE

COORDINATOR(S)

Dr. P. Kalpana Devi, Professor - ECE

Dr. R. Rajkumar, Associate Professor - ECE

Dr. V. Diana Earshia, Assistant Professor - ECE



School of Electrical and Communication

Department of Electronics and Communication Engineering

In association with

IEEE Student Branch Chapter , Vel Tech

Organize

IEEE Madras Section Sponsored

Faculty Development Program

on

“AI Enabled VLSI Architectures for Signal Processing Applications”



20th - 25th July 2026



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ABOUT THE INSTITUTION

Vel Tech offers UG, PG and Ph.D. programs in both engineering and management. The institution has made distinctive progress since the founding in 1997, by adopting the unique and trending educational methodologies. It is a private deemed-to-be university, established in 2008. The university has been recognized by UGC as the "Category 1 Deemed to be University" and approved by AICTE, New Delhi as well. Vel Tech is the first member from India to be part of CDIO, an innovative educational framework for producing the next generation of engineers. Vel Tech has been recognized as the best engineering institution and ranked A++ in NAAC and 87th position in NIRF India Rankings 2025. The Technology Business Incubator (TBI) at Vel Tech is recognized as a Centre of Excellence under the NIDHI PRAYAS scheme by the Government of India. It is also the first institution in India to house the NVIDIA DGX-I AI Supercomputer and has established six Centres of Excellence to foster innovation, research, and entrepreneurship.

ABOUT THE SCHOOL

The School of Electrical and Communication at Vel Tech imparts knowledge in electrical, electronics and communication engineering and related allied areas. Students actively engage in nationally reputed clubs such as the Robotics Club and the Programming Club, which inspire them to explore and develop their skills. Many of the faculty members hold doctorates from prestigious institutions in India and abroad. Students from the School participate in high-impact national and international projects, including collaborations on small satellite systems with industry partners. The School consistently delivers engineers of exceptional calibre, equipping graduates to assume prominent roles in both academia and industry.

ABOUT THE DEPARTMENT

The Department of Electronics & Communication Engineering (ECE) was established in 1997 and has grown into a vibrant center for academic excellence, research, and innovation. The department offers B.Tech. in Electronics and Communication Engineering, accredited by the National Board of Accreditation (NBA) under Tier-I, M.Tech in Embedded Systems and Technologies and Ph.D. programmes approved by AICTE. To align with technological advancements, the B.Tech. programme offers specializations in AI&ML, AI&DS, Cyber Security, IoT and Integrated Circuit Technology. The department comprises 117 faculty members, including 116 with doctorates, with over 102 recognized research supervisors actively engaged in teaching, research and consultancy. With advanced laboratories and dedicated research centers, the department supports interdisciplinary work in Signal processing, Communication, VLSI, Embedded Systems, IoT and Networks. The department also maintains a strong placement record, with students placed in reputed industries such as BOSCH, IBM, WIPRO, TCS, L&T, Tech Mahindra, VA Tech Wabag Ltd., Microchip and Zebronic.

VISION

To emerge as a Centre of academic and research excellence in Electronics and Communication Engineering by fostering innovation, interdisciplinary learning and global engagement, contributing to sustainable technological development and societal progress.

MISSION

- To strengthen Outcome-based education through a strong foundation in core concepts, experiential learning and adoption of emerging technologies.
- To nurture and enhance a research-driven academic environment that encourages innovation, entrepreneurship and interdisciplinary research addressing societal and sustainable development challenges.
- To augment professional competence and global exposure through academic, industry and international collaborations for joint research, internships and academic mobility.

- To develop ethically grounded and socially responsible professionals by fostering teamwork, leadership, diversity and gender equity within a supportive academic and research ecosystem.

ABOUT THE FDP

The rapid advancement of Artificial Intelligence (AI) is transforming the design of modern VLSI architectures, enabling high-performance, low-power and intelligent signal processing systems. AI-enabled hardware capable of executing complex algorithms efficiently with minimal latency and power consumption. This FDP aims to provide participants with comprehensive knowledge of AI-driven VLSI design methodologies, hardware accelerators and intelligent signal processing architectures. The program covers the latest developments in AI hardware and FPGA/ASIC implementation. Participants will gain insights into recent research trends, practical implementation techniques through hands-on sessions.

About IEEE Madras Section

The IEEE Madras Section is a professional body of technology volunteers dedicated to advancing engineering and technological excellence in Tamil Nadu through conferences, technical activities and professional events. IEEE Madras Section started in 1973 as the Tamil Nadu Sub-section of the Bangalore Section. The Sub-section attained Section status on 28th April 1978. IEEE Madras Section Golden Jubilee Year 2025, was celebrated in a grand manner. It regularly organizes Hackathons, TechClaves, Symposiums, Technical Presentation contests, and expert lectures by eminent personalities from emerging technology domains.

KEY FOCUS AREAS

- Fundamentals of AIML for Hardware Design.
- Modern VLSI Design Flow and EDA.
- Digital Signal Processing Architectures.
- AI for Signal Processing Applications.
- FPGA-based AI and DSP Implementation.
- Low-Power and Energy-Efficient VLSI Architectures.
- High-Speed Units and DSP Processor Design.
- Verification, Testing and Validation of AI Hardware.