



Vel Tech

Ranganathan Dr. Sagunthala
R&D Institute of Science and Technology
(Deemed to be University) Estd. as 3rd U.G. Act, 1986

B.Tech. Electrical and Electronics Engineering Semester End Examinations Time Table May/June - 2026

Regular Courses

Applicable for 2024-25 Admitted Batch Only

Duration : 3Hrs

Time FN: 10.00 am to 01.00 pm

AN: 02.00 pm to 05.00 pm

S.No.	Date & Session	CourseCode	Course Name
1	14.05.2026 FN	10210MA107	Statistics and Numerical Methods
2	16.05.2026 FN	10211EE104	Digital Electronics
3	19.05.2026 FN	10211EE105	Linear Control Systems
		10211EE113	Electronic Circuits
4	21.05.2026 FN	10211EE103	AC Machines
		10212EE203	Electrical Machines
5	23.05.2026 FN	10211EE106	Measurements and Instrumentation
6	26.05.2026 FN	10212EE165	Medical Electronics
		10210CH104	Environmental Science and Sustainability
7	29.05.2026 FN	10212EE903	Embedded Software Engineering- II
		10210ME104	Project Management and Finance
		10212EE101	Computer Architecture
8	01.06.2026 FN	10210ME101	Design Thinking
9	03.06.2026 FN	10212EE104	Data Structures and Algorithms
		10210ME103	Innovation and Entrepreneurship
10	05.06.2026 FN	10212EE112	EV Drives and Control
		10210ME102	Universal Human Values
11	08.06.2026 FN	10212EE113	Energy Storage Systems



S. P. Kumar
Controller of Examinations