



Vel Tech
Rangarajan Dr. Sagunthala
R&D Institute of Science and Technology
DEEMED TO BE
UNIVERSITY
(Est. in 3 of UGC Act, 1956)
Avadi, Chennai



mobiveil
a GlobalLogic company

Vel Tech Rangarajan Dr.Sagunthala R&D Institute of Science and Technology

School of Electrical and Communication

Department of Electronics and Communication Engineering

&

Office of Industry Relations

organizes

Five Days Faculty Development Programme

on

**“Industry-Oriented System Verilog and UVM with
Hands-on Verification and Synopsys Tool Practices”**

2nd – 6th March 2026 | **Vel Tech - Chips to Startup (C2S)
Centre of Excellence (1412)**



**02nd
March
09:30 AM**

Inauguration

Chief Guest

Ms. Shyamala Devi

Senior Engineer/ Trainer

Mobiveil Technologies Pvt Ltd



ECE Gallery
Hall (1601)

CONVENOR(S)

Dr. R. S. Valarmathi

Dean - SoEC

Dr. S. Jana

HoD-ECE

ORGANIZING CHAIR(S)

Dr. A. Mutharasan

Assistant Professor – IR – Coordinator

Dr. S. Shiyamala

Professor- ECE

ORGANIZING CO-CHAIR(S)

Dr. N. Nalini

Associate Professor - ECE

Dr. P. Rajendiran

Associate Professor- ECE

With the Blessings of

Col. Prof. Dr. Vel R. Rangarajan

Founder President & Chancellor

Late. Dr. Sagunthala Rangarajan

Foundress President

Prof. Dr. Rajat Gupta

Vice Chancellor



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#PassionateDoers

AGENDA

Time	Event
09.30 - 09.35 a.m	Invocation
09.36 - 09.40 a.m	Lighting of Kuthuvilaku
09.41 - 09.45 a.m	Welcome Address Dr. S. Jana <i>Prof. & HoD-ECE</i>
09.46 – 09.50 a.m	About the Event Dr. P. Chandrakumar, <i>Prof. & Dean- SoEC</i>
09.51 – 09.55 a.m	Felicitation Address Dr. R. S. Valarmathi <i>Prof. & Dean- SoEC</i>
09.56 – 10.00 a.m	Introduction of the Chief Guest Dr. S. Shiyamala Professor – ECE
10.01 – 10.10 a.m	Inaugural Address Ms. Shyamala Devi <i>Senior Engineer/Trainer</i> <i>Mobiveil Technologies Pvt Ltd</i>
10.11 – 10.15 a.m	Vote of Thanks Dr. A. Mutharasan <i>Assistant Professor / IR -Coordinator</i>