

Laboratory Name: Centre for Semiconductor Design & Testing

Venue: 29102

Laboratory In-Charge: Dr. Vignesh Prasanna N and Dr.J.Brittohari

Qualification: M.E., Ph.D.,

DESCRIPTION ABOUT THE LABORATORY

- The objective of this Centre of Excellence (CoE) is to provide a platform to build a strong relationship between Vel Tech and Tessolve for exploring the technology development in VLSI design and Embedded IO.
- To improve capabilities of common interest and engage in research projects. Through this CoE, we intend to develop a special relationship, which shall enable effective knowledge sharing to the extent possible from time to time, exchange technical skills, bridge the platform between industry and institution.
- To enhance students and faculty to fill their skill gaps and to create a conducive atmosphere for the betterment of the society.

LABORATORY SPACE AND STUDENT ALLOCATION

Area of the Laboratory	: 148.36 Sq.m
No. of students per session	: 30
Batch Size	: Single
Weekly Utilization Status	: Open Access for
Research & Development Activities	

COST OF THE LABORATORY

The lab was equipped with VLSI, Embedded IoT boards, Sensors, AI ML and Robo kits of a total worth ₹31,86,800 in which Tessolve Semiconductors Pvt Ltd has sponsored worth ₹6,13,000.

OBJECTIVES

- To develop and foster strategic linkages such as National / International conferences, industrial seminars & workshops, guest lectures, joint research proposal, collaborative projects and consultancy.
- To offer credit-based program or open elective courses for students.
- To provide technical advisory in the academic activities.

LIST OF SPONSORED BOARDS



“Empowering Future Semiconductor Engineers through Industry-Driven Learning”

KEY ACTIVITIES

- VACs, Workshops & FDPs
- Hands-on EDA Tool Training
- VLSI & Embedded Projects
- Hackathons & Design Challenges
- Industry Expert Talks

PHD SCHOLARS & RESEARCH FOCUS

Doctoral Research Areas

- VLSI Design and Verification
- Embedded System and IOT
- AI Hardware and FPGA
- VLSI Signal Image processing and Communication
- VLSI Security

STUDENT BENEFITS

- Hands-on Industry Tool Exposure
- Internship Opportunities
- Certification Programs
- Real-time Project Experience
- Placement Support