


Vel Tech Rangarajan Dr. Sagunthala R&D Institute of Science and Technology


School of Electrical and Communication

Department of Electronics and Communication Engineering

in association with

 **23rd**
Feb 2026
 **09.00 a.m**

NOVEL INTEGRATED ELECTRONICS LABS
(NINE LABS, IIT GUWAHATI)

 **Gallery Hall**
(1601)

Organize

Five - Days Workshop

on

“VLSI DESIGN USING OPEN SOURCE TOOLS”

Sponsored By

Ministry of Electronics and Information Technology (MeitY)



Inauguration

Chief Guest

Ms. Indira Iyer Almeida

Program Director

The OpenROAD Initiative

Convenors

Dr. R. S. Valarmathi

Dean - SoEC

Dr. S. Jana

HoD - ECE

Organizing Chair(S)

Dr. Debashish Pal

Assistant Professor – ECE

Dr. K. Aanandha Saravanan

Professor- ECE

Dr. S. Shiyamala

Professor- ECE

Organizing Co-Chair(S)

Dr. D. Preethi

Associate Professor- ECE

Dr. J. Arun Kumar

Associate Professor- ECE

Dr. R. Sathesh Raaj

Associate Professor- ECE

With the Blessings of

Col. Prof. Dr. Vel R. Rangarajan

Chancellor & Founder President

Late. Dr. Sagunthala Rangarajan

Foundress President

Prof. Dr. Rajat Gupta

Vice Chancellor