

## WHO CAN ATTEND

UG/PG Students , Faculty, Research Scholars  
Industrial Experts

## RESOURCE PERSONS

**Dr. Deepak Mishra**, Professor, Department of Avionics, Indian Institute of Space Science and Technology (IIST), Trivandrum, Kerala

**Dr. R. Sivakumar**, Professor, Department of Civil Engineering, SRM Institute of Science and Technology, Kattankulathur.

**Dr. K. Thilagavathi**, Assistant Professor, Department of ECE, Kumaraguru College of Technology, Coimbatore

**Dr. R. Anand**, Assistant Professor, Department of EEE, Amrita School of Engineering, Coimbatore

**Dr. S. Vimalathithan**, Assistant Professor, Department of IT, St. Joseph's College of Engineering, Chennai.

**Dr. Divya Priya B**, Assistant Professor, Institute of Remote Sensing, College of Engineering Guindy Campus, Anna University, Chennai.

## REGISTRATION FEE

### Faculty/ Professionals

IEEE SPS Members : Rs. 280  
IEEE Members : Rs. 370  
Non IEEE Members : Rs. 560

### Students/ Research Scholars

IEEE SPS Student Members : Rs. 200  
IEEE Student Members : Rs. 280  
Non IEEE Student Members : Rs. 370

*Note : Travel and Accommodation allowance will be provided as per IEEE SPS Norms*

## Account Details

Name : Vel Tech Rangarajan Dr. Sagunthala  
R&D Institute of Science and  
Technology Trust

Account Number : 75330400000013

Bank : Bank of Baroda

Branch : Vel Tech Engineering College

IFSC : BARBOVJVELT (Fifth character is zero)

## REGISTRATION LINK:

<https://forms.gle/xMRHXSUFVNkBPtUk9>

## DATES TO REMEMBER

Last date to register online - 06.02.2026

Intimation of Selection - 07.02.2026



## WITH THE BLESSINGS OF

**Col. Prof. Vel Dr. R. Rangarajan**  
Founder President & Chancellor  
**Dr. Sagunthala Rangarajan**  
Foundress President

## PATRONS

**Prof. Dr. Rajat Gupta**  
Vice Chancellor  
**Prof. Dr. E. Kannan**  
Registrar

## ADVISORS

**Prof. Dr. Raju Shanmugam**,  
Dean Academics  
**Prof. Dr. P. Suresh**,  
Dean - International Relations & HRDC  
**Prof. Dr. P. Chandrakumar**,  
Dean R&D, Industry & Incubation  
**Prof. Dr. P. Sakthivel**, Chairman, IEEE MAS  
**Prof. Dr. N. Venkateswaran**, Chairman, IEEE SPS - MAS  
**Prof. Dr. S. Brindha**, Vice Chair, IEEE SPS - MAS  
**Prof. Dr. R. S. Valarmathi**, Dean – SoEC, IEEE-SBC, Vel Tech  
**Prof. Dr. S. Jana**, Head - ECE

## Chair(s)

**Dr. P. Kalpana Devi**, Professor – ECE  
**Dr. R. Rajkumar**, Associate Professor – ECE

## Co-Chair(s):

**Dr. M. Anto Bennet**, Professor - ECE  
**Dr. V. Diana Earshia**, Assistant Professor – ECE  
**Dr. M. Ajisha**, Assistant Professor – ECE

## CONTACT DETAILS

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[ajisham@veltech.edu.in](mailto:ajisham@veltech.edu.in)



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<https://chat.whatsapp.com/J8Bb0olszvd6Fwr6vMOeGk>



**School of Electrical and Communication  
Department of Electronics and Communication  
Engineering**

**in association with  
IEEE Signal Processing Society, Madras Chapter  
IEEE SPS Student Branch Chapter, Vel Tech**

**Organize**

**IEEE SPS Sponsored Seasonal School on  
“Hyper-spectral Imaging for Sustainable  
Development: from Spectral Data to Societal  
Impact”**

**9 – 13 February, 2026**



[www.veltech.edu.in](http://www.veltech.edu.in)  
1800 212 7669

No.42, Avadi - Vel Tech Road,  
Vel Nagar, Avadi,  
Chennai - 600062.

### ABOUT THE INSTITUTION

Vel Tech offers UG, PG and Ph.D. programs in both engineering and management. The institution has made distinctive progress since the founding in 1997, by adopting the unique and trending educational methodologies. It is a private deemed to-be university, established in 2008. The university has been recognized by UGC as the “Category 1 Deemed to be University” and approved by AICTE, New Delhi as well. Vel Tech is the first member from India to be part of CDIO, an innovative educational framework for producing the next generation of engineers. Vel Tech has been recognized as the best engineering institution and ranked A++ in NAAC and 87<sup>th</sup> position in NIRF India Rankings 2024. The Technology Business Incubator (TBI) at Vel Tech is recognized as a Centre of Excellence under the NIDHI PRAYAS scheme by the Government of India. It is also the first institution in India to house the NVIDIA DGX-I AI Supercomputer and has established six Centres of Excellence to foster innovation, research, and entrepreneurship.

### ABOUT THE SCHOOL

The School of Electrical and Communication at Vel Tech imparts knowledge in electrical, electronics and communication engineering and related allied areas. Students actively engage in nationally reputed clubs such as the Robotics Club and the Small Device Programming Club, which inspire them to explore and develop their skills. Many of the faculty members hold doctorates from prestigious institutions in India and abroad. Students from the School participate in high-impact national and international projects, including collaborations on small satellite systems with industry partners. The School consistently delivers engineers of exceptional calibre, equipping graduates to assume prominent roles in both academia and industry.

### ABOUT THE DEPARTMENT

The Department of Electronics & Communication Engineering (ECE) was established in 1997 and has grown into a vibrant center for academic excellence, research, and innovation. The department offers B.Tech. in Electronics and Communication Engineering, accredited by the National Board of Accreditation (NBA) under Tier-I, M.Tech. in Embedded Systems and Technologies, and Ph.D. programmes approved by AICTE. To align with technological advancements, the B.Tech. programme offers specializations in AI&ML, AI&DS, Cyber Security, IoT and Integrated Circuit Technology. The department comprises 118 faculty members, including 116 with doctorates, with over 102 recognized research supervisors actively engaged in teaching, research, and consultancy. With advanced laboratories and dedicated research centers, the department supports interdisciplinary work in Signal processing, Communication, VLSI, Embedded Systems, IoT and Networks. The department also maintains a strong placement record, with students placed in reputed industries such as BOSCH, IBM, WIPRO, TCS, L&T, Tech Mahindra, VA Tech Wabag Ltd., Microchip, and Zebtronics.

### VISION

To be a centre of academic excellence through quality education and cutting-edge research in the diversified fields of electronics and communication engineering to meet the global challenges and produce high quality professionals.

### MISSION

- To enrich the knowledge of graduate engineers for global requirements by promoting quality education through innovative pedagogical practices.
- To create an ambience of academic excellence by engaging in cutting-edge research and undertaking collaborative projects with academia and industry.
- To develop competence by inculcating human and moral values with leadership and professional skills.

### ABOUT THE SEASONAL SCHOOL

The 2025 Cycle-2 IEEE SPS Seasonal School on “Hyperspectral Imaging for Sustainable Development: From Spectral Data to Societal Impact” is a specialized advanced training program supported by the IEEE Signal Processing Society with approved funding. The program focuses on hyperspectral data acquisition, processing, and machine learning techniques for sustainable development applications in agriculture, environmental monitoring, and smart infrastructure. It features lectures, tutorials, and hands-on sessions delivered by resource persons from reputed universities and leading research institutions, along with experts from industry and technology companies, fostering academia–industry interaction. The school aims to strengthen technical expertise, promote collaboration, and translate spectral data analytics into real-world societal and environmental impact.

### KEY FOCUS AREAS

- Introduction to Hyperspectral Imaging (HSI)
- HSI Data Acquisition and Preprocessing
- Signal Processing Techniques for HSI
- Dimensionality Reduction and Feature Extraction
- Machine Learning and AI for HSI Analysis
- Interpretation of Hyperspectral Data
- Applications of HSI in Sustainable Development
- Hands-on Training and Practical Case Studies
- Industry Insights and Startup Perspectives
- Research and Future Directions

### HANDS-ON / PRACTICAL SESSIONS

- Basic Operations, Exploration, and Visualization.
- Implementing Noise Reduction & Preprocessing.
- Feature Extraction and Dimensionality Reduction.
- Deep Learning Hands-on for Hyperspectral Image Analysis.

**LAB VISIT:** Hyperspectral Image Capturing.