



Vel Tech
Rangarajan Dr. Sagunthala
R&D Institute of Science and Technology
(Deemed to be University) Est. 1979 UGC Act 1956

B.Tech. Electrical and Electronics Engineering
Semester End Examinations Time Table Dec - 2025
Regular & Arrear Courses
Applicable for Passed-out Batches and Extended Registration

Duration : 3Hrs

Time FN: 10.00 am to 01.00 pm

AN: 02.00 pm to 05.00 pm

S.No.	Date	Course Code	Course Name
1	01.12.2025 FN	1151EE112	Electrical Machine Design
		1151EE110	Power System Analysis
2	02.12.2025 FN	1151EE201	Electromagnetic Fields
		1150MA201	Applied Statistics
3	03.12.2025 FN	1151EE113	Microprocessor & Microcontroller
		1154IT107	Python Programming Fundamentals
4	04.12.2025 FN	1150MG101	Project Management and Finance
5	05.12.2025 FN	1151EE104	AC Machines
6	06.12.2025 FN	1151EE106	Control Systems
7	08.12.2025 FN	1151EE107	Measurements and Instrumentation
8	09.12.2025 FN	1151EE108	Transmission and Distribution
9	10.12.2025 FN	1151EE111	Power System Operation & Control
10	11.12.2025 FN	1151EE114	Discrete Time Signal Processing
11	12.12.2025 FN	1152EE106	Smart Grid
12	13.12.2025 FN	1152EE118	Solid State Drives
13	15.12.2025 FN	1152EE135	Renewable Energy Sources

Controller of Examinations

