



Vel Tech
Rangarajan Dr. Sagunthala
R&D Institute of Science and Technology
(Deemed to be University) Estd. as per UGC Act, 1956

B.Tech. Electronics & Communication Engineering
Semester End Examinations Time Table Nov/Dec - 2025

Regular Courses

Applicable for 2024 Admitted Batch Only

Duration : 3Hrs

Time FN: 10.00 am to 01.00 pm

AN: 02.00 pm to 05.00 pm

S.No.	Date	Course Code	Course Name
1	17.11.2025 FN	10210MA104	Fourier Series and Transform Techniques
2	19.11.2025 FN	10211EC101	Circuit Theory
3	21.11.2025 FN	10210EN201	Professional Communication - I
		10211EC103	Digital Electronics
4	24.11.2025 FN	10211EC110	Data Communication Networks
5	26.11.2025 FN	10211EC102	Analog Electronics
6	28.11.2025 FN	10211EC104	Linear Integrated Circuits
7	01.12.2025 FN	10213CS102	Data Structures
8	03.12.2025 FN	10210ME104	Project Management and Finance
9	05.12.2025 FN	10210CH104	Environmental Science and Sustainability
		10210ME101	Design Thinking

G. L. Anand

Controller of Examinations

S. S. S. S.

