



Vel Tech

Ranganathan Dr. Sagunthala
R&D Institute of Science and Technology
(Deemed to be University) Estd. in 3 of UGC Act, 1956

B.Tech. Electrical and Electronics Engineering
Semester End Examinations Time Table Nov/Dec - 2025
Regular Courses

Applicable for 2023 Admitted Batch Only

Duration : 3Hrs

Time FN: 10.00 am to 01.00 pm

AN: 02.00 pm to 05.00 pm

S.No.	Date	Course Code	Course Name
1	18.11.2025 FN	10211EE101	Circuits Analysis
		10211EE107	Transmission and Distribution
2	20.11.2025 FN	10211EE102	DC Machines and Transformers
		10211EE114	Linear Integrated Circuits
3	22.11.2025 FN	10210ME104	Project Management and Finance
		10212EE154	Renewable Energy Sources
4	25.11.2025 FN	10210MA104	Fourier Series and Transform Techniques
		10211EE112	Microprocessor and Microcontrollers
5	27.11.2025 FN	10212EE105	Computer Networks and Communication
		10212EE135	Special Electrical Machines
		10212EE153	Electrical Safety & Safety Management
		10213CE101	Building Materials
		10213EN106	Effective Group Discussion
6	29.11.2025 FN	10212EE102	Operating Systems
		10212EE113	Energy Storage Systems
7	02.12.2025 FN	10210CS101	Problem Solving using C
		10211EE201	Electromagnetic Fields
8	04.12.2025 FN	10210CH104	Environmental Science and Sustainability
		10211EE113	Electronic Circuits
9	06.12.2025 FN	10210ME103	Innovation and Entrepreneurship
10	09.12.2025 FN	10213CS105	Computer Networks
11	11.12.2025 FN	10210ME101	Design Thinking



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Controller of Examinations