



B.Tech. - Electronics & Communication Engineering (2015 - Regulation)

Semester End Examinations Time Table Dec - 2023

Arrear Course for Passed-out Batches

Duration : 3.00 Hrs

Time - FN : 10.00 am to 01.00 pm

AN : 02.00 pm to 05.00 pm

S.No.	Date	Course Code	Course Name
1	15.12.2023 FN	1150MA104	Transforms and Partial Differential Equations
		1151EC112	Discrete Time Signal Processing
		1151EC115	VLSI Design
		1152EC133	DSP Algorithms and Architecture
		1152EC140	Cellular Mobile Communication
		1152EC256	Basics of Python Programming
		1154AE102	Disaster Management
2	16.12.2023 FN	1150MA202	Engineering Mathematics - I
		1151EC105	Linear Integrated Circuits
		1151EC106	Analog and Digital Control Systems
		1151EC110	Microprocessor and Microcontroller
		1152EC103	Silicon Validation
3	18.12.2023 FN	1150CS201	Problem Solving Using C
		1150MA103	Engineering Mathematics - II
		1151EC102	Electric Circuit Analysis
		1151EC111	Data Communication Networks
		1153BM101	Body Area Networks
4	19.12.2023 FN	1150MA201	Applied Statistics
		1151EC104	Digital Electronics
		1151EC109	Analog Communication Systems
		1151EC113	Wireless Digital Communication
		1152EC118	Embedded Control System
5	20.12.2023 FN	1150GE101	Biology for Engineers
		1151EC114	Waveguides and Antennas
		1151EC116	Optical and Microwave Engineering
		1154MA102	Fundamentals of Fuzzy Mathematics
6	21.12.2023 FN	1151EC107	Signals and Systems
		1152EC102	Low Power VLSI Design
		1152EC147	Fiber Lasers and Applications
7	22.12.2023 FN	1151EC101	Mathematics for Electronics and Communication Engineers
		1153IT102	Database Management System
8	23.12.2023 FN	1154MA103	Introduction to Number Theory for Engineers



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Controller of Examinations

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