



Vel Tech
Rangarajan Dr. Sagunthala
R&D Institute of Science and Technology
(Deemed to be University Estd. u/s 3 of UGC Act, 1956)

End Semester Arrear Examinations through Online Mode
M.Tech Degree Programmes
Department of Electronics and Communication Engineering
TIME TABLE AUGUST -2020
Applicable for 2016,2017 & 2018 Admitted Batches Only

Duration : 2 Hrs

Time - FN: 10.00 am to 12.00 Noon

AN: 02.00 pm to 04.00 pm

Si.No	Date	Embedded-Sys
1	08.08.2020 FN	2160MA101 - Applied Mathematics and Statistics/ 2161EC107 - Advanced Digital System Design/ 2162EC109 - Analog and Mixed Signal IC Design/ 2162EC137 - Real Time System with FPGA
2	08.08.2020 AN	2161EC101 - Semiconductor Physics and Device Modeling/ 2162EC108 - Sensors and Actuators/ 2162EC111 - Internet of Things/ 2163MG401 - Research Methodology
3	09.08.2020 FN	2161EC102 - DSP Processor and Architecture/ 2161EC205 - Embedded OS and Device Drivers/ 2162EC117 - Principles of Multimedia and Multimedia Architectures
4	09.08.2020 AN	2162EC101 - Embedded Networks and Protocol
5	10.08.2020 FN	2161EC108 - Embedded Control System Design/ 2161EC201 - System Design
6	10.08.2020 AN	2161EC109 - Software Technology for Embedded System
7	11.08.2020 FN	2162EC103 - Embedded Design and Development Systems
8	12.08.2020 AN	2163GE401 - Business Communication

Controller of Examinations

For any queries regarding timetable send
email to onlineexams@veltech.edu.in or contact mobile number 73388 03051