

From

J.GNANA ARUN JOHNSON (TTS2205),
Assistant Professor,
Department of ECE,
Vel Tech Dr.RR & Dr.S.R Technical University,
Chennai.

C-1815
B. Senthil
12/7/16
B. Senthil Kumar

To

The Vice Chancellor,
Vel Tech Dr.RR & Dr.S.R Technical University,
Chennai.

Respected Sir,

Gp 2.0.3

Sub: Requesting Permission to attend 3 days FDP, Sanction of on duty and Requesting Financial Assistance-Reg

I wish to attend 3 days FDP on "Mathematical Tools for Signal, Image Processing and Communications" during July 15th-17th, 2016 organized by VIT Vellore. It will be very useful for me to guide the students to do their research work. I kindly request to grant permission to attend the above mentioned FDP, Provide Financial Assistance (Registration Fee + Accommodation) and sanction on duty from 15/07/2016 to 17/07/2016 (3Days). Kindly do the needful.

Expenditure

Registration Fee = Rs.3000

Accommodation = Rs.1500

Total = Rs.4500

12/7

Date: 06/07/2016

Encl: FDP Details

Yours Faithfully

J. Gnana Arun Johnson

(J.GNANA ARUN JOHNSON)

if interested
budget register
J. no. 22
J. no. 22

The faculty is allotted 1 yr BEE course. Since 16th and 17th are non working days, the faculty may be given one day OD on 15th for workshop.

He is in signal processing domain group in ECE department

submitted to Director Academics

11/5/16

10/7/16
A. Kumar
11/5/16

FDP on

Mathematical Tools for Signal, Image Processing and Communications

ADVISORY COMMITTEE

Chief Patron
Dr. G. Viswanathan
Chancellor

Patrons
Mr. Sankar Viswanathan, Vice President
Dr. Sekar Viswanathan, Vice President
Mr. G.V. Selvam, Vice President
Dr. Anand A Samuel, Vice-Chancellor
Dr. S. Narayanan, Pro Vice-Chancellor
Dr. V. Raju, Pro Vice-Chancellor

Advisors
Dr. Elizabeth Rufus,
Dean, School of Electronics Engineering,
VIT University.

Prof. S. Renuga Devi,
Associate Professor and Head
Department of Communication Engineering
School of Electronics Engineering
VIT University
Vellore-632 014

Coordinator
Dr. K. Vinodh Babu
Associate Professor
Department of Communication Engineering
School of Electronics Engineering
VIT University, Vellore-632 014
+91-9994311391
vinodhbabu@vit.ac.in

Important Dates:
Last date for registration: 5th July, 2016
FDP Date: 15-17 July, 2016
Prospective participants are requested to fill up the registration form with DD and send it to the coordinator.

Full Name

Gender: Male / Female

Designation:

Organization:

Address:

Mobile No:

E-Mail:

Accommodation required: Yes/ No
(On Nominal Payment Basis)

DD No:

Amount: Date:

Declaration: The information provided is true to the best of my knowledge. If selected, I agree to abide by the rules and regulations of the course and attend the course for the entire duration.

Place:
Date:
Signature of the Participant

Registration Fees:
Faculty/Industry: Rs. 3000/-
Research Scholars/Students: Rs. 2000/-
(Registration Fees Inclusive of 14.5% service tax)
DD in favor of VIT University, payable at Vellore, Tamil Nadu.



FDP on

Mathematical Tools for Signal, Image Processing and Communications

15-17 July, 2016

Organized by
Department of Communication Engineering,
School of Electronics Engineering,
VIT University, Vellore

About The Institute

VIT University was established with the aim of providing quality higher education at par with international standards. It persistently seeks and adopts innovative methods to improve the quality of higher education on a consistent basis. The campus has a cosmopolitan atmosphere with students from all corners of the globe. Experienced and learned teachers are strongly encouraged to nurture the students. The global standards set at VIT in the field of teaching and research spur us on in our relentless pursuit of excellence.

Our Memoranda of Understanding with various international universities are our major strength. They provide for an exchange of students and faculty and encourage joint research projects for the mutual benefit of these universities. VIT University is the first Indian Institute to get International Accreditation from IET and EI, UK. VIT University obtained the highest possible grade of "A" from NAAC during the re-accreditation process in 2015. With steady steps, we continue our march forward. We look forward to meeting you here at VIT.

About School of Electronics Engineering

School of Electronics Engineering (SENSE) has 103 faculty members who have done their UG and PG degrees from the top-notch universities. The faculty members of this school are consistently doing well in teaching and research. The School offers B.Tech (Electronics and Communication Engineering), M.Tech (Communication Engineering), M.Tech (VLSI Design), M.Tech (Embedded System), M.Tech (Sensor Technology) and M. Tech (Nano Technology), M.S. by Research and Ph.D. in Engineering. Both B.Tech and M.Tech programmes attract the brightest students of the country and abroad every year. B.Tech

Topics to be covered:

Module1:

Relating signals & systems with vectors & matrices. Norms, inner products, vector spaces, Projection matrices, least squares and weighted least squares problem formulations in matrix form, MMSE filtering, factorizations-LU, Cholesky, QR.

Module 2:

Eigen vectors, Eigen values- Diagonalization of matrices, PCA, SVD, Special matrices-Permutation, Toeplitz, Circulant. DFT, circular convolution, Givens rotations, House holder transformation, Total least squares problems.

Module3: Applications of vectors and matrices

Eigen filter design, signal space spectrum estimation -MUSIC, ESPRIT, Transceiver for OFDM, GFDM. Image representation, Image compression and analysis, Pattern recognition and classification.

MATLAB sessions will follow to get hands on experience of various tools discussed in the lectures.

Resource Persons:

Dr. G. Ramachandra Reddy, Senior Professor
Department of Communication Engineering
School of Electronics Engineering
VIT University, Vellore-632 014

Dr. Ravindra Dhuli, Associate Professor
Department of Communication Engineering
School of Electronics Engineering
VIT University, Vellore-632 014

Dr. K. Vinoth Babu, Associate Professor
Department of Communication Engineering
School of Electronics Engineering
VIT University, Vellore-632 014

(Electronics and Communication Engineering), is accredited by the Engineering Accreditation Commission of ABET. The placement record of the School has always been impressive. Almost 100% of the students get job from the campus placement and many of them are getting it in core companies every year. We encourage the students to do engineering projects during their B.Tech and M.Tech degrees. The School has state-of-the-art laboratories in almost all the areas of Electronics, Communication, VLSI, Embedded, Sensors and Nano Technology. The School has the latest simulation tools to cater to various specializations and is equipped with facilities for measurement, characterization and synthesis of experimental as well as theoretical results. Every year, students get scholarships to do their final year projects abroad under the Semester Abroad Program (SAP).

The School has many industry sponsored advanced laboratories for doing research. The School has signed MoUs with many foreign Universities, research organizations and Industries from where students get benefits for their R&D Work / Projects from the MoUs. The School is actively involved in R&D activities and has sponsored projects from various agencies like DRDO, DST, ISRO (RESPOND), and BRNS.

About The FDP

Mathematical tools for Signal, Image Processing and Communications:

This FDP is aimed at making the faculty and researchers comfortable with various mathematical tools used for the advanced research and analysis in the field of signal, image processing and communications. After the successful completion of this FDP, the participants will be able to do independent analysis and develop mathematical modeling of the research ideas. Comfort levels while reading all standard research papers will considerably increase by learning the language of mathematics used in the field.

DEPARTMENT OF COMMUNICATION ENGINEERING



This is to certify that Dr. Mr/Ms J. Gnana Arun Johnson, Asst. Prof. of Vel Tech Dr. RR & Dr. SR Technical University has successfully completed the 3 Day (15-07-16 to 17-07-16) Conference / Value Added Programme / Workshop / FDP on Mathematical Tools for Signal, Image Processing and Communications organized by the Department of Communication Engineering, School of Electronics Engineering, VIT University, Vellore 632014.

K. Rajan
Course Coordinator

J. Ramesh
Head of the Department
Department of Communication Engineering

J. Ramesh
Dean
SENSE

Checked



VEL TECH

Dr.RR & Dr.SR TECHNICAL UNIVERSITY

University u/s 3 of UGC Act, 1956
VEL TECH RANGARAJAN Dr.SAGUNTHALA R&D INSTITUTE OF SCIENCE AND TECHNOLOGY
ACCREDITED BY NAAC WITH 'A' GRADE



TTS 2205

Ref:/FDP/ VTU / 274

03 August 2016

C-1215

To

Present Address:

GNANA ARUN JOHNSON. J
NO. 28/49, E - BLOCK,
COMMUNITY HALL ST., MMDA,
ARUMBAKKAM, CHENNAI - 106
9962347122

Permanent Address:

Same as communication
address

Sir/Madam,

We hereby acknowledge the receipt of the following certificates :

1. CERTIFICATE FOR PARTICIPATION FDP ON MATHEMATICAL TOOLS FOR SIGNAL IMAGE ORGANIZED BY THE DEPT. OF COMMUNICATION ENGG. VIT UNIVERSITY. ON 15-JUL-2016 - 17-JUL-2016.

For VEL TECH RANGARAJAN Dr. SAGUNTHALA R&D INSTITUTE OF SCIENCE AND TECHNOLOGY

REGISTRAR.

Prepared by ANNAN
Registrar

Verified By:

Accepted by:

Tech Dr. RR & Dr. SR Technical University
(B. S. Raju)

(S. S. Raju) (GNANA ARUN JOHNSON. J)