



Vel Tech

Rangarajan Dr. Sagunthala

R&D Institute of Science and Technology

(Deemed to be University Estd. u/s 3 of UGC Act, 1956)



Sl. No. : 7392

Reg. No. : 12REEE1001

The Board of Management of
Veltech Rangarajan Dr. Sagunthala R & D Institute of
Science and Technology (Deemed to be University)

hereby makes known that

K KARTHIKEYAN

S/O VIJAYAMANI K

has been admitted to the Degree of
Doctor of Philosophy

in

ELECTRICAL AND ELECTRONICS ENGINEERING

having been certified by duly appointed examiners to be

qualified to receive the same in the year **JUL-2019**

in accordance with the provisions of

UGC Regulations 2009 (for the award of Ph.D. Degree)

Title of the Thesis:

**INVESTIGATION OF POWER SYSTEM STABILITY
IMPROVEMENT USING OPTIMIZATION TECHNIQUES**

Given under the seal of this Deemed to be University

Dated : 13.08.2019




Registrar


Vice Chancellor


President

**REPORT OF THE BOARD OF EXAMINERS FOR THE PUBLIC ORAL EXAMINATION
IN THE CASE OF Mr. KARTHIKEYAN K, (Reg. No. 12REEE1001), PART-TIME
RESEARCH SCHOLAR, DEPARTMENT OF ELECTRICAL AND ELECTRONICS
ENGINEERING, VEL TECH RANGARAJAN Dr. SAGUNTHALA R&D INSTITUTE OF
SCIENCE AND TECHNOLOGY, AVADI, CHENNAI- 600062.**

The Public Viva-Voce examination of Mr. Karthikeyan K, (Reg. No. 12REEE1001), on his thesis entitled "*Investigation of Power System Stability Improvement using Optimization Techniques*" submitted for the award of Ph.D degree in Electrical and Electronics Engg of Vel Tech Rangarajan Dr. Sagunthala R&D Institute of Science and Technology has been conducted as per the following schedule:

Date: 13/07/2019, Saturday at 11.00 a.m.

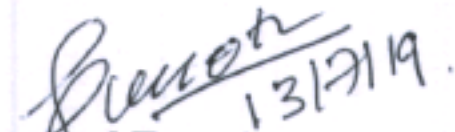
Venue: Edison Hive (Block No.: 08), Vel Tech

External Examiners: Dr. P.K. Hota
Professor
Department of Electrical Engineering,
Veer Surendra Sai University of Technology,
Burla – 768018, Odisha.
Phone: 09439859929
Email: pkhota_ee@vssut.ac.in

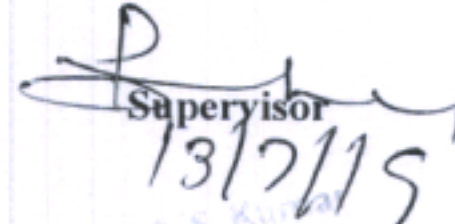
Dr. V Senthil Kumar
Professor,
Department of Electrical and Electronics Engineering
Anna University,
Tamil Nadu,
Chennai – 600025.
Phone: 9444100661
Email: sensang@annauniv.edu

Supervisor and Convener: Dr. P.K. Dhal, (Professor, Vel Tech)

Based on the candidate's exposition of his thesis and his answers to the questions raised by the examiners and other participants, the Board of Examiners has resolved to recommend that the performance of the candidate in his Ph.D. Oral Examination is excellent and Good and declare to have passed his Ph.D. Oral Examination and further recommend the award of Ph.D degree in Electrical and Electronics Engg of Vel Tech Rangarajan Dr. Sagunthala R&D Institute of Science and Technology to the candidate.


13/7/19.
External Examiner
(Prof. P.K. Hota)


13/7/2019
Expert Member
(Dr. V. SENTHIL KUMAR)


13/7/19
Supervisor
Prof. V. S. S. Kumar
Vice Chancellor
18/7/19
Vel Tech
Rangarajan Dr. Sagunthala
Institute of Science and Technology
AVADI, CHENNAI-600062



Vel Tech

Rangarajan Dr. Sagunthala
R&D Institute of Science and Technology
(Deemed to be University Estd. u/s 3 of UGC Act, 1956)

PROCEEDINGS OF THE Ph.D. ORAL EXAMINATION OF Mr./Ms. KARTHIKEYAN.K HELD AT 11.00 A.M./P.M. ON 13/07/2019 IN THE EDISON HIVE, VEL TECH RANGARAJAN Dr. SAGUNTHALA R&D INSTITUTE OF SCIENCE AND TECHNOLOGY, AVADI, CHENNAI-600 062.

The Ph.D. Oral Examination of Mr./Ms. KARTHIKEYAN.K (Reg.No. 12REEE1001) on the thesis INVESTIGATION OF POWER SYSTEM STABILITY IMPROVEMENT USING OPTIMIZATION TECHNIQUES has been conducted on 13/07/19 at 11.00 A.M./P.M. in the EDISON HIVE Vel Tech Rangarajan Dr. Sagunthala R&D Institute of Science and Technology, Avadi, Chennai-600 0062.

Members Present

- | | |
|-----------------------|-----------------------|
| 1. DR.P.K.HOTA | Thesis Examiner |
| 2. DR.V.SENTHIL KUMAR | Expert Member |
| 3. DR.P.K.DHAL | Supervisor & Convener |

The research scholar Mr./Ms. KARTHIKEYAN.K has presented the salient features of his/her research work. The members of the Board and audiences have posed the questions related to the subject. The questions raised by the Foreign and Indian Examiners have also been put to the scholar. The scholar has answered all the questions from the Board Members and the audiences. The scholar's performance in the Ph.D. Oral Examination is excellent and Good.

Based on the scholar's research work, oral presentation and, clarifications and answers to the questions from the Examiners and audiences, the Board unanimously recommends to accept the Ph.D. Thesis for the award of Degree of Doctor of Philosophy in Electrical and Electronics Engg. in the School of Electrical and Communication, Vel Tech Rangarajan Dr. Sagunthala R&D Institute of Science and Technology to Mr./Ms. KARTHIKEYAN.K.

1. [Signature] 13/7/19
2. [Signature] 13/7/2019
3. DR.P.K.DHAL

Thesis Examiner
Expert Member
Supervisor & Convener

[Signature]
13/7/19

[Signature]
Prof. V. S. S. Kumar
Vice Chancellor
Vel Tech
Rangarajan Dr. Sagunthala
R&D Institute of Science and Technology
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