



Vel Tech

Rangarajan Dr. Sagunthala
R&D Institute of Science and Technology
(Deemed to be University Estd. u/s 3 of UGC Act, 1956)

Date: 23/09/2020

Ref No: VT/DRS/O/Ph.D.-Adm/Summer-2020-21/ECE-005

To

Mr.N. Revanth
Plot No 4, MSS Balaji Villas Phase 2,
Near Rayalaseema University, Kurnool,
Andhra Pradesh, India 518007
Mobile: 7780639212, E-mail Id: nrevanth90@gmail.com

Dear Candidate,

Sub: Intimation - Provisional admission to Full Time Ph.D. Programme Summer Session 2020-21- Reg.

Ref: Application No: VTDAS200293 Academic Year 2020-21 Reg.

With reference to your application cited above for the admission to Ph.D. Programme (Regulations 2016) for the academic year 2020-21 (Summer Session), it is to inform you that based on the recommendation of Department Research Committee, you are provisionally admitted under Full Time mode to carry out your research work leading to the award of Ph.D. degree Programme in **Electronics and Communication Engineering**, at Vel Tech Rangarajan Dr.Sagunthala R&D Institute of Science and Technology (Deemed to be University).

You are permitted to carry out your research work under the guidance of Dr. S Shiyamala, Associate Professor, Department of Electronics and Communication Engineering, School of Electrical and Communication. (Mobile No: 9940906470, Email id; drshiyamala@veltech.edu.in)

You are requested to produce all your original certificates at the time of certificate verification process which will be held soon after normalcy return. You are hereby informed to contact your Research Supervisor to discuss about your research work and further clarification.

Copy to:

1. The Dean Academics
2. The Dean, School of Electrical and Communication
3. The Head, Department of Electronics and Communication Engineering
4. Research Supervisor
5. Office of the Dean Research Studies (Dossier)


REGISTRAR

Prof. Dr. E. Kannan
Registrar

Vel Tech
Rangarajan Dr. Sagunthala
R&D Institute of Science and Technology
(Deemed to be University Estd. u/s 3 of UGC Act, 1956)

No.42, Avadi-Vel Tech Road, Vel Nagar,
Avadi, Chennai - 600 062, Tamil Nadu, India.
Toll Free : 1800 212 7669
Landline : +91 44 2684 0262 / 2684 0605
Email : registrar@veltech.edu.in
Website : www.veltech.edu.in



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To
The Head of the Department,
Department of ECE,

Sir/Madam,

Sub: Ph.D Scholar (Full Time) -Research Associate – Intimation - Regarding

Mr. N Revanth (VTD 841) PhD scholar (Full Time) is assigned to Dr S Shiyamala, System on Chip Laboratory / Department of ECE. He is advised to work in the area of VLSI as Research Associate. He will be also assisting the research group in performing theoretical and experimental analysis, report writing, research articles and submission of research grant applications.

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2. The Dean, School of Electrical and Communication
3. The Head, Department of Electronics and Communication Engineering
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REGISTRAR

Prof. Dr. E. Kannan
Registrar

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