



Vel Tech

Rangarajan Dr. Sagunthala
R&D Institute of Science and Technology
(Deemed to be University Estd. u/s 3 of UGC Act, 1956)

Date: 23/09/2020

Ref No: VT/DRS/O/Ph.D.-Adm/Summer-2020-21/EEE-003

To

Ms. Divya R
No: 34, Karnan Street Extn.,
Kovilpathagai, Avadi,
Chennai, Tamil Nadu
India 600062
Mobile: 7402134629, E-mail Id: divyarajendran.kr@gmail.com

Dear Candidate,

Sub: Intimation - Provisional admission to Full Time Ph.D. Programme Summer Session 2020-21- Reg.

Ref: Application No: VTDAS200075 Academic Year 2020-21 Reg.

With reference to your application cited above for the admission to Ph.D. Programme (Regulations 2016) for the academic year 2020-21 (Summer Session), it is to inform you that based on the recommendation of Department Research Committee, you are provisionally admitted under Full Time mode to carry out your research work leading to the award of Ph.D. degree Programme in **Electrical and Electronics Engineering**, at Vel Tech Rangarajan Dr.Sagunthala R&D Institute of Science and Technology (Deemed to be University).

You are permitted to carry out your research work under the guidance of **Dr. K. Karunanithi**, Associate Professor, Department of Electrical and Electronics Engineering, School of Electrical and Communication. (Mobile No: 9080394411, Email id: drkkarunanithi@veltech.edu.in)

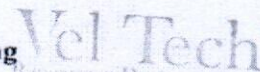
You are requested to produce all your original certificates at the time of certificate verification process which will be held soon after normalcy return. You are hereby informed to contact your Research Supervisor to discuss about your research work and further clarification.


REGISTRAR

Prof. Dr. E. Kannan
Registrar

Copy to:

1. The Dean Academics
2. The Dean, School of Electrical and Communication
3. The Head, Department of Electrical and Electronics Engineering
4. Research Supervisor
5. Office of the Dean Research Studies (Dossier)


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Avadi, Chennai - 600 062, Tamil Nadu, India.
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Email : registrar@veltech.edu.in
Website : www.veltech.edu.in



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To

The Head of the Department,
Department of EEE.

Sir/Madam,

Sub: Ph.D Scholar (Full Time) -Research Associate – Intimation - Regarding

Ms. Divya R (VTD 835) PhD scholar (Full Time) is assigned to Dr K Karunanithi, Centre for Battery Engineering / Department of EEE. She is advised to work in the area of Battery as Research Associate. She will be also assisting the research group in performing theoretical and experimental analysis, report writing, research articles and submission of research grant applications.

Copy to:

1. The Dean Academics
2. The Dean, School of Electrical and Communication
3. The Head, Department of Electrical and Electronics Engineering
4. Research Supervisor
5. Office of the Dean Research Studies (Dossier)

REGISTRAR

Prof. Dr. E. Karunan
Registrar

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