



Vel Tech

Rangarajan Dr. Sagunthala
R&D Institute of Science and Technology
(Deemed to be University Estd. u/s 3 of UGC Act, 1956)

Date: 20/12/2018

Ref No: VT/REG/Ph.D-Adm/Winter-2018-19/ EEE - 3

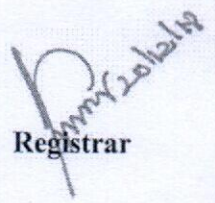
To

Mr. VIJAYA BHASKAR K
23012, arettamma colony
puttur, Andhra Pradesh,
India – 517583.
9966342090.

Sir,

This is to inform that **Mr. VIJAYA BHASKAR K** is provisionally admitted into Ph.D Programme in the **Department of Electrical and Electronics Engineering**, Vel Tech Rangarajan Dr.Sagunthala R&D Institute of Science and Technology (Deemed to be University) for Winter 2018 Batch of the academic year 2018-19. He is permitted to carry out his research work under the guidance of **Dr A Abudhahir**, **Department of Electrical and Electronics Engineering**.

He shall contact the research supervisor and pursue his research work as per the Ph.D Regulations-2016 of Vel Tech Rangarajan Dr.Sagunthala R&D Institute of Science and Technology (Deemed to be University) and the candidate is entitled for an amount of Rs.15,000/- per month as stipend.


Registrar

Copy to:

1. The Director, Academics.
2. The Dean, Research Studies.
3. The Head, Department of Electrical and Electronics Engineering.
4. Dr A Abudhahir, Research Supervisor.

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To
The Head of the Department,
Department of EEE.

Sir/Madam,

Sub: Ph.D Scholar (Full Time) -Research Associate – Intimation - Regarding

Mr. Vijaya Bhaskar K (VTD 652) PhD scholar (Full Time) is assigned to Dr S Ramesh, Power Sysem Simulation Laboratory / Department of EEE. He is advised to work in the area of Power Systems as Research Associate. He will be also assisting the research group in performing theoretical and experimental analysis, report writing, research articles and submission of research grant applications.


REGISTRAR
Prof. Dr. E. Kannan
Registrar

Vel Tech
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R&D Institute of Science and Technology
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