



Vel Tech

Rangarajan Dr. Sagunthala
R&D Institute of Science and Technology
(Deemed to be University Estd. u/s 3 of UGC Act, 1956)

Date: 25/06/2018

Ref No: VT/REG/Ph.D-Adm/2018-19/Summer-18/ECE-1

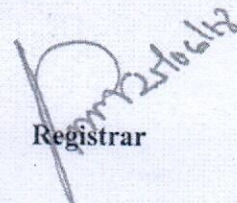
To

Ms P KALAIARASI
10, 3RD CROSS STREET,
ANNANAGAR,
ANAKAPUTHUR, PAMMAL,
CHENNAI-600075.

Madam,

This is to inform that **Ms Kalaiarasi P** is provisionally admitted into Ph.D (Full Time) Programme in the Department of Electronics and Communication Engineering, Vel Tech Rangarajan Dr.Sagunthala R&D Institute of Science and Technology (Deemed to be University) for Summer 2018 Batch of the academic year 2018-19. She is permitted to carry out her research work under the guidance of **Dr P Esther Rani**, Department of Electronics and Communication Engineering.

She shall contact the research supervisor and pursue her research work as per the Ph.D Regulations-2016 of Vel Tech Rangarajan Dr.Sagunthala R&D Institute of Science and Technology (Deemed to be University) and the candidate is entitled for an amount of Rs.15,000/- per month as stipend.


Registrar

Prof. Dr. E. Kannan
Registrar

Copy to:

1. The Director, Academics.
2. The Dean, Research Studies.
3. The Head, Department of Electronics and Communication Engineering.
4. Dr P Esther Rani, Research Supervisor.

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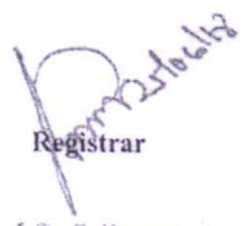
To

The Head of the Department,
Department of ECE

Sir/Madam,

Sub: Ph.D Scholar (Full Time) -Research Associate – Intimation - Regarding

Ms. Kalaiyarasi.P (VTD 603) PhD scholar (Full Time) is assigned to Dr P Esther Rani, Signal Processing Laboratory / Department of ECE. She is advised to work in the area of Machine Learning-Image Processing as Research Associate. She will be also assisting the research group in performing theoretical and experimental analysis, report writing, research articles and submission of research grant applications.


Registrar

Prof. Dr. E. Kannan
Registrar

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