



Vel Tech

Rangarajan Dr. Sagunthala
R&D Institute of Science and Technology
(Deemed to be University Estd. u/s of UGC Act, 1956)

Date: 12/09/2020

Ref No: VT/DRS/O/Adm-Int/Summer-2020-21/EEE/008

To

Mr. Tarek Abyad
Bab-Qinnasrin,
Old City,
Aleppo,
Syria.

Dear Candidate,

Sub:- Ph.D. Programme - Provisional Admission – Regarding.

This is to inform that your provisional admission into Ph.D. (Full Time) Programme in the Department of **Electrical and Electronics Engineering**, Vel Tech Rangarajan Dr.Sagunthala R&D Institute of Science and Technology (Deemed to be University) for Summer Session of the academic year 2020-21 is confirmed based on the recommendation of Departmental Research Committee. Your admission is subject to verification of original certificates at the time of reporting.

As per the Institution norms, your tuition fee is 100% waived and you are entitled to have stipend of Rs. 15,000/- per month.

12/09/2020
Registrar

Prof. Dr. E. Kannan
Registrar

Copy to:

1. The Dean, Academics.
2. The Dean, Research Studies.
3. The Dean, International Relations.
4. The Head, Department of EEE.
5. The Research Supervisor.

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No.42, Avadi-Vel Tech Road, Vel Nagar,
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Email : registrar@veltech.edu.in
Website :www.veltech.edu.in



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To
The Head of the Department,
Department of EEE,

Sir/Madam,
Sub: Ph.D Scholar (Full Time) -Research Associate – Intimation - Regarding

Mr. Tarek Abyad (VTD 805) PhD scholar (Full Time) is assigned to Dr P Chandrasekar, Power Electronics and Drivers Laboratory / Department of EEE. He is advised to work in the area of Power Electronics as Research Associate. He will be also assisting the research group in performing theoretical and experimental analysis, report writing, research articles and submission of research grant applications.

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3. The Dean, International Relations.
4. The Head, Department of EEE.
5. The Research Supervisor.

Registrar

Prof. Dr. E. Kannan
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